

# **HD74AC366**

## Hex Inverter Buffer with 3-State Output

REJ03D0270-0200Z (Previous ADE-205-391 (Z)) Rev.2.00 Jul.16.2004

#### **Features**

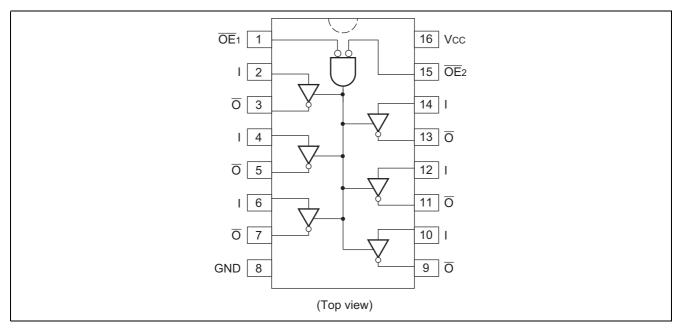
- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- Outputs Source/Sink 24 mA
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74AC366FPEL	SOP-16 pin (JEITA)	FP-16DAV	FP	EL (2,000 pcs/reel)
HD74AC366RPEL	SOP-16 pin (JEDEC)	FP-16DNV	RP	EL (2,500 pcs/reel)

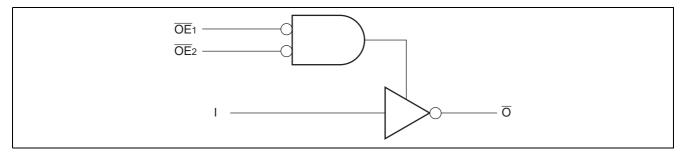
Notes: 1. Please consult the sales office for the above package availability.

2. The packages with lead-free pins are distinguished from the conventional products by adding V at the end of the package code.

### **Pin Arrangement**



## **Logic Symbol**



#### **Pin Names**

 $\overline{OE}_1$ ,  $\overline{OE}_2$  3-State Output: Enable Input (Active Low)

I Inputs O Outputs

#### **Truth Table**

Inputs	Output		
ŌE₁	$\overline{OE}_2$	I	0
L	L	L	Н
L	L	Н	L
X	Н	X	Z
Н	X	X	Z

H: High Voltage LevelL: Low Voltage LevelX: ImmaterialZ: High Impedance

### **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Condition	
Supply voltage	V <sub>CC</sub>	-0.5 to 7	V		
DC input diode current	I <sub>IK</sub>	-20	mA	$V_1 = -0.5V$	
		20	mA	V <sub>I</sub> = Vcc+0.5V	
DC input voltage	V <sub>I</sub>	-0.5 to Vcc+0.5	V		
DC output diode current	I <sub>OK</sub>	-50	mA	$V_{O} = -0.5V$	
		50	mA	V <sub>O</sub> = Vcc+0.5V	
DC output voltage	V <sub>o</sub>	-0.5 to Vcc+0.5	V		
DC output source or sink current	Io	±50	mA		
DC V <sub>CC</sub> or ground current per output pin	I <sub>CC</sub> , I <sub>GND</sub>	±50	mA		
Storage temperature	Tstg	-65 to +150	°C		

### **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Condition
Supply voltage	V <sub>cc</sub>	2 to 6	V	
Input and Output voltage	$V_{I}, V_{O}$	0 to V <sub>cc</sub>	V	
Operating temperature	Та	-40 to +85	°C	
Input rise and fall time	tr, tf	8	ns/V	$V_{CC} = 3.0V$
(except Schmitt inputs)				$V_{CC} = 4.5 \text{ V}$
$V_{IN}$ 30% to 70% $V_{CC}$				V <sub>CC</sub> = 5.5 V

#### **DC Characteristics**

Item	Sym- bol	Vcc (V)	٦	Га = 25°(	C	Ta = -40 to +85°C		Unit	Condition
			min.	typ.	max.	min.	max.		
Input Voltage	V <sub>IH</sub>	3.0	2.1	1.5	_	2.1	_	V	$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$
		4.5	3.15	2.25	_	3.15	_		
		5.5	3.85	2.75	—	3.85	—		
	V <sub>IL</sub>	3.0	—	1.50	0.9	—	0.9		$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$
		4.5	_	2.25	1.35	_	1.35		
		5.5	_	2.75	1.65	_	1.65		
Output voltage	V <sub>OH</sub>	3.0	2.9	2.99	_	2.9	_	V	$V_{IN} = V_{IL}$ or $V_{IH}$
		4.5	4.4	4.49	_	4.4	_		$I_{OUT} = -50 \mu A$
		5.5	5.4	5.49	_	5.4	_		
		3.0	2.58	_	_	2.48	_		$V_{IN} = V_{IL} \text{ or } V_{IH}$ $I_{OH} = -12 \text{ mA}$
		4.5	3.94	_	_	3.80	_		$I_{OH} = -24 \text{ mA}$
		5.5	4.94	_	_	4.80	_		$I_{OH} = -24 \text{ mA}$
	V <sub>OL</sub>	3.0	_	0.002	0.1	_	0.1		$V_{IN} = V_{IL} \text{ or } V_{IH}$
		4.5	_	0.001	0.1	_	0.1		$I_{OUT} = 50 \mu\text{A}$
		5.5	_	0.001	0.1	_	0.1		
		3.0	_	_	0.32	_	0.37		$V_{IN} = V_{IL} \text{ or } V_{IH}$ $I_{OL} = 12 \text{ mA}$
		4.5	_	_	0.32	_	0.37		$I_{OL} = 24 \text{ mA}$
		5.5	_	_	0.32	_	0.37		$I_{OL} = 24 \text{ mA}$
Input leakage current	I <sub>IN</sub>	5.5	_	_	±0.1	_	±1.0	μА	$V_{IN} = V_{CC}$ or GND
3 State current	I <sub>OZ</sub>	5.5	_	_	±0.5	_	±5.0	μА	$V_{IN(OE)} = V_{IL}, V_{IH}$ $V_{IN} = V_{CC} \text{ or GND}$
Dumamia autorit	1	<i></i>				00		A	$V_{OUT} = V_{CC}$ or GND
Dynamic output current*	I <sub>OLD</sub>	5.5	_	_	_	86	_	mA	V <sub>OLD</sub> = 1.1 V
	I <sub>OHD</sub>	5.5	_	_	_	<del>-75</del>	_	mA	V <sub>OHD</sub> = 3.85 V
Quiescent supply current	I <sub>CC</sub>	5.5		_	8.0		80	μΑ	$V_{IN} = V_{CC}$ or ground

<sup>\*</sup>Maximum test duration 2.0 ms, one output loaded at a time.

#### **AC Characteristics**

			Ta = +25°C C <sub>1</sub> = 50 pF			C to +85°C 50 pF		
Item	Symbol	V <sub>cc</sub> (V)*1	Min	Typ	Max	Min	Max	Unit
Propagation delay	t <sub>PLH</sub>	3.3	1.0	7.0	9.0	1.0	10.0	ns
		5.0	1.0	5.0	7.0	1.0	7.5	
Propagation delay	t <sub>PHL</sub>	3.3	1.0	7.0	9.0	1.0	10.0	ns
		5.0	1.0	4.5	7.0	1.0	7.5	
Enable time	t <sub>zH</sub>	3.3	1.0	9.0	13.0	1.0	13.5	ns
		5.0	1.0	7.0	9.5	1.0	10.0	
Enable time	t <sub>ZL</sub>	3.3	1.0	10.0	12.5	1.0	13.5	ns
		5.0	1.0	7.5	9.5	1.0	10.0	
Disable time	t <sub>HZ</sub>	3.3	1.0	9.5	12.0	1.0	12.5	ns
		5.0	1.0	7.5	10.0	1.0	10.5	
Disable time	t <sub>LZ</sub>	3.3	1.0	9.0	12.5	1.0	13.5	ns
		5.0	1.0	7.0	10.0	1.0	10.5	

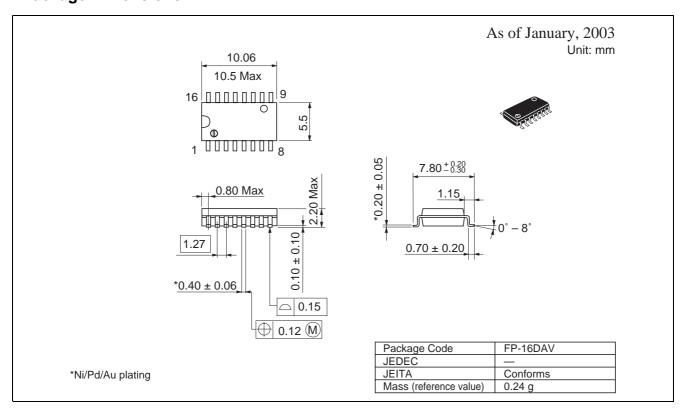
Note: 1. Voltage Range 3.3 is  $3.3 \text{ V} \pm 0.3 \text{ V}$ Voltage Range 5.0 is  $5.0 \text{ V} \pm 0.5 \text{ V}$ 

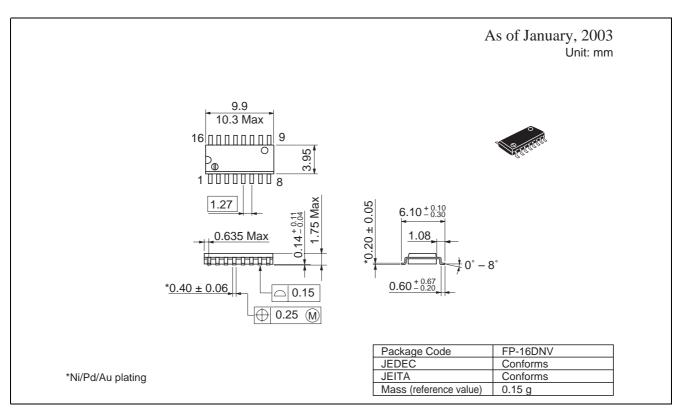
### HD74AC366

## Capacitance

Item	Symbol	Тур	Unit	Condition
Input capacitance	C <sub>IN</sub>	4.5	pF	V <sub>CC</sub> = 5.5 V
Power dissipation capacitance	C <sub>PD</sub>	40.0	pF	V <sub>CC</sub> = 5.0 V

#### **Package Dimensions**





Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs!

1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.

2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.

- therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.

  The information described here may contain technical inaccuracies or typographical errors.

  Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.

  Please also pay attention to information published by Renesas Technology Corp. by various means, including the Renesas Technology Corp. Semiconductor home page (http://www.renesas.com).

  4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.

  5. Renesas Technology Corp. semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
- use.
  6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.
  7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
  Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
  8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.



**RENESAS SALES OFFICES** 

http://www.renesas.com

Renesas Technology America, Inc. 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500 Fax: <1> (408) 382-7501

Renesas Technology Europe Limited.

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, United Kingdom Tel: <44> (1628) 585 100, Fax: <44> (1628) 585 900

**Renesas Technology Europe GmbH**Dornacher Str. 3, D-85622 Feldkirchen, Germany
Tel: <49> (89) 380 70 0, Fax: <49> (89) 929 30 11

Renesas Technology Hong Kong Ltd. 7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2375-6836

**Renesas Technology Taiwan Co., Ltd.** FL 10, #99, Fu-Hsing N. Rd., Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. 26/F., Ruijin Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1, Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001